

STAR Trigger BDB Crate Layout

front:

	CDB#9 (ZDC)
	CDB#8 (ZDC pixel)
	CDB#7 (ZDC pixel)
	CDB#6 (west large tile)
	CDB#5 (east large tile)
	CDB#4 (west small tile)
	CDB#3 (east small tile)
	CDB#2 (west small tile)
	CDB#1 (east small tile)
mika scaler	
MVMIE167 (bdb.starpc)	

back: 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21

DIB (ZDC)
DIB (ZDC pixel)
DIB (ZDC pixel)
TAC #5 (ZDC)
TAC #4 (BBC west large tiles)
DIB (west large tile)
DIB (east large tile)
TAC #3 (BBC east large tiles)
DIB (west small tile)
DIB (east small tile)
TAC #2 (BBC west small tiles)
DIB (west small tile)
TAC #1 (BBC east small tiles)
DIB (east small tile)
TLA (BBC/ZDC signals to RHIC)
713M interface to MVME167

1A8-2 Rack

19-oct-2004 j.engelage